Amendments to the Claims

This listing of the Claims will replace all prior versions and listings of the claims in this patent application.

Listing of the Claims

1-46. (canceled)

47. (currently amended) A method of forming copper interconnects in the fabrication of an integrated circuit device comprising:

providing a substrate having a point of electrical contact in or on a surface of said substrate and having a first insulating layer overlying said substrate,

forming a first copper interconnect to said electrical contact through an opening in said first insulating layer wherein said first copper interconnect comprises a first single via and an overlying copper line wherein said first single via is isolated from other vias formed through said first insulating layer and and an overlying first copper linelocated elsewhere on said substrate; and

forming a first slot in said first copper line overlying said first single via wherein said first slot is adjacent to an interface between said first single via and said first copper line and wherein said first slot provides stress relief at the said interface of said first single via and said first copper line.

48. (currently amended) The method according to Claim 47 further comprising:

forming a second insulating layer overlying said first copper interconnect; and

forming a second copper interconnect through said second insulating layer to said first copper interconnect wherein said second copper interconnect comprises a second single via and a second copper line overlying said second single via wherein said second single via is isolated from other vias and a second overlying copper lineformed through said second insulating layer elsewhere on said substrate.

- 49. (previously presented) The method according to Claim 47 wherein said first copper line has a width of more than about 0.2μm.
- 50. (previously presented) The method according to Claim 48 wherein said second copper line has a width of more than about 0.2μm.
- 51. (currently amended) The method according to Claim 48 wherein said <u>first</u> slot in said first copper line provides stress relief at the interface of said second single via and said second copper line.
- 52. (currently amended) The method according to Claim 48 further comprising forming a second slot in said second copper line overlying said second single via wherein said second slot is adjacent to an interface between said second single via and said second copper line and wherein said second slot in said second copper line provides stress relief at the said interface of said second single via and said second copper line.

- 53. (currently amended) The method according to Claim 47 wherein said <u>first</u> slot comprises:
- a first slot <u>portion</u> spaced a first distance from said first single via in an X-direction;
- a second slot <u>portion</u> spaced a second distance from said first single via in an X-direction opposite from said X-direction of said first slot <u>portion</u>; and
- a third slot <u>portion</u> spaced a third distance from said first single via in a Y-direction.
- 54. (currently amended) The method according to Claim 53 wherein said first, second, and third slot portions have a rectangular or square shape.
- 55. (currently amended) The method according to Claim 53 wherein said third slot <u>portion</u> overlaps said first slot <u>portion</u> by a fourth distance and wherein said third slot <u>portion</u> overlaps said second slot <u>portion</u> by a fifth distance.
- 56. (previously presented) The method according to Claim 53 wherein said first distance is between about 0.1 and 0.4 μm .
- 57. (previously presented) The method according to Claim 53 wherein said second distance is between about 0.1 and 0.4μm.

- 58. (previously presented) The method according to Claim 53 wherein said third distance is between about 0.05 and 0.3μm.
- 59. (previously presented) The method according to Claim 55 wherein said fourth distance is between about 0.1 and 0.5 µm.
- 60. (previously presented) The method according to Claim 55 wherein said fifth distance is between about 0.1 and 0.5 µm.
- 61. (currently amended) The method according to Claim 53 wherein said first slot portion has dimensions of between about 0.1 and 0.5μm by between about 0.5 and 1.0 μm.
- 62. (currently amended) The method according to Claim 53 wherein said second slot portion has dimensions of between about 0.1 and 0.5μm by between about 0.5 and 1.0 μm.
- 63. (currently amended) The method according to Claim 53 wherein said third slot <u>portion</u> has dimensions of between about 0.1 and 0.5μm by between about 0.9 and 1.5 μm.
- 64. (currently amended) The method according to Claim 52 wherein said second slot in said second copper line comprises:
- a first slot <u>portion</u> spaced a first distance from said second single via in an X-direction;

- a second slot <u>portion</u> spaced a second distance from said second single via in an X-direction opposite from said X-direction of said first slot<u>portion</u>; and
- a third slot <u>portion</u> spaced a third distance from said second single via in a Y-direction.
- 65. (currently amended) The method according to Claim 64 wherein said first, second, and third slots portions have a rectangular or square shape.
- 66. (currently amended) The method according to Claim 64 wherein said third slot portion overlaps said first slot portion by a fourth distance and wherein said third slot portion overlaps said second slot portion by a fifth distance.
- 67. (previously presented) The method according to Claim 64 wherein said first distance is between about 0.1 and 0.4 μ m, said second distance is between about 0.1 and 0.4 μ m, and said third distance is between about 0.05 and 0.3 μ m.
- 68. (previously presented) The method according to Claim 66 wherein said fourth distance is between about 0.1 and 0.5μm and said fifth distance is between about 0.1 and 0.5μm.
- 69. (currently amended) The method according to Claim 64 wherein said first slot <u>portion</u> has dimensions of between about 0.1 and 0.5μm by between about 0.5 and 1.0 μm, said second slot <u>portion</u> has dimensions of between about 0.1 and 0.5μm by between about 0.5

and 1.0 μ m, and said third slot portion has dimensions of between about 0.1 and 0.5 μ m by between about 0.9 and 1.5 μ m.

70. (currently amended) A method of forming copper interconnects in the fabrication of an integrated circuit device comprising:

providing a first copper line over a substrate;

forming a insulating layer overlying said first copper line;

forming a copper interconnect to said first copper line through an opening in said insulating layer wherein said copper interconnect comprises a single via and an overlying copper line wherein said first single via is isolated from other vias formed through said insulating layer and and an overlying second copper linelocated elsewhere on said substrate; and

forming a slot in one or more of said first copper line underlying said single via and said second copper lines overlying said single via wherein said slot is adjacent to an interface between said single via and said one or more of said first and second copper lines wherein said slot provides stress relief at the said interface of said single via and said one or more of said first and second copper lines.

71. (currently amended) The method according to Claim 70 wherein said slot comprises:

a first slot <u>portion</u> spaced a first distance from said first single via in an X-direction;

a second slot <u>portion</u> spaced a second distance from said first single via in an X-direction opposite from said X-direction of said first slot; and

a third slot <u>portion</u> spaced a third distance from said first single via in a Y-direction.

- 72. (currently amended) The method according to Claim 71 wherein said first, second, and third slot portions have a rectangular or square shape.
- 73. (currently amended) The method according to Claim 71 wherein said third slot portion overlaps said first slot portion by a fourth distance and wherein said third slot portion overlaps said second slot portion by a fifth distance.
 - 74. (previously presented) The method according to Claim 71 wherein said first distance is between about 0.1 and 0.4 μ m, said second distance is between about 0.1 and 0.4 μ m, and said third distance is between about 0.5 and 3.0 μ m.
 - 75. (previously presented) The method according to Claim 73 wherein said fourth distance is between about 0.1 and 0.5 µm and said fifth distance is between about 0.1 and 0.5 µm.
 - 76. (currently amended) The method according to Claim 71 wherein said first slot portion has dimensions of between about 0.1 and 0.5µm by between about 0.5 and 1.0µm, said second slot portion has dimensions of between about 0.1 and 0.5µm by between about 0.5 and 1.0µm, and said third slot portion has dimensions of between about 0.1 and 0.5µm by between about 0.9 and 1.5µm.

77. (previously presented) The method according to Claim 70 wherein said first and second copper lines have a width of more than about $0.2\mu m$.

78 - 87. (canceled)